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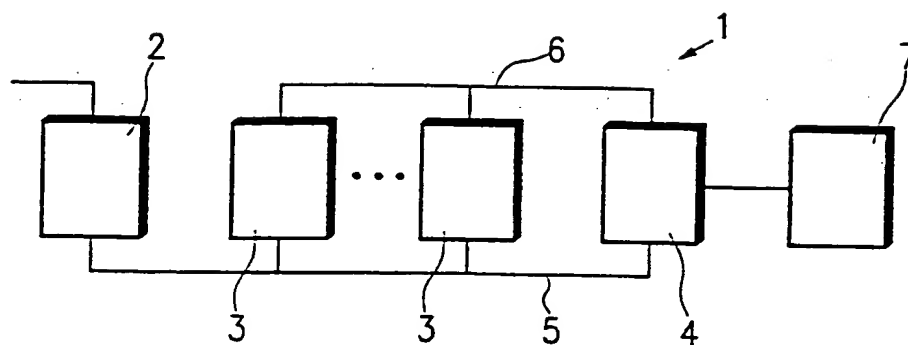


FIG. 1

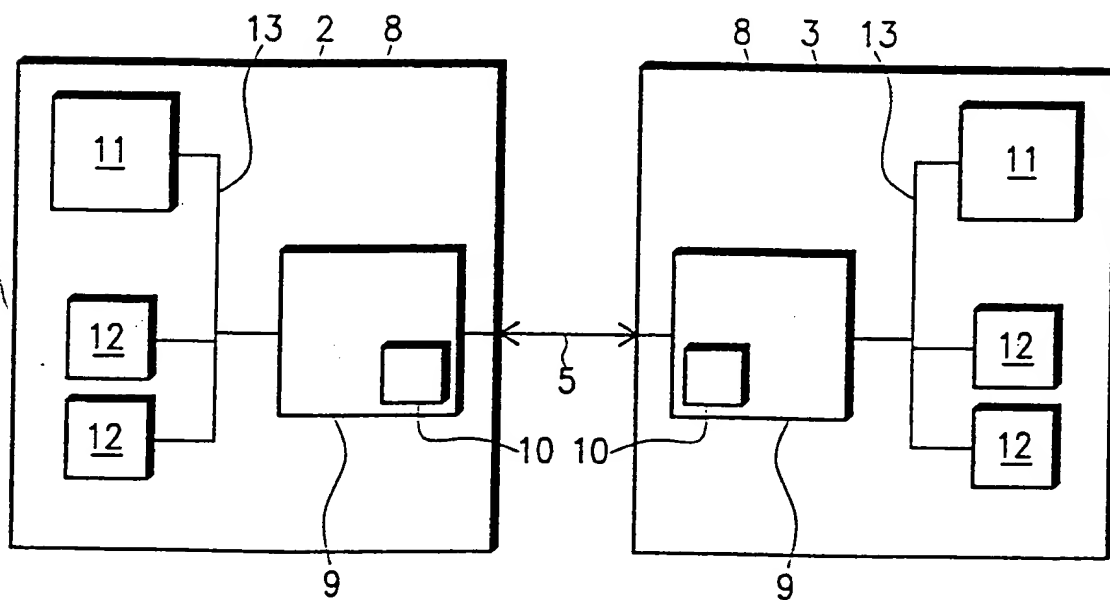


FIG. 3

FIG. 1

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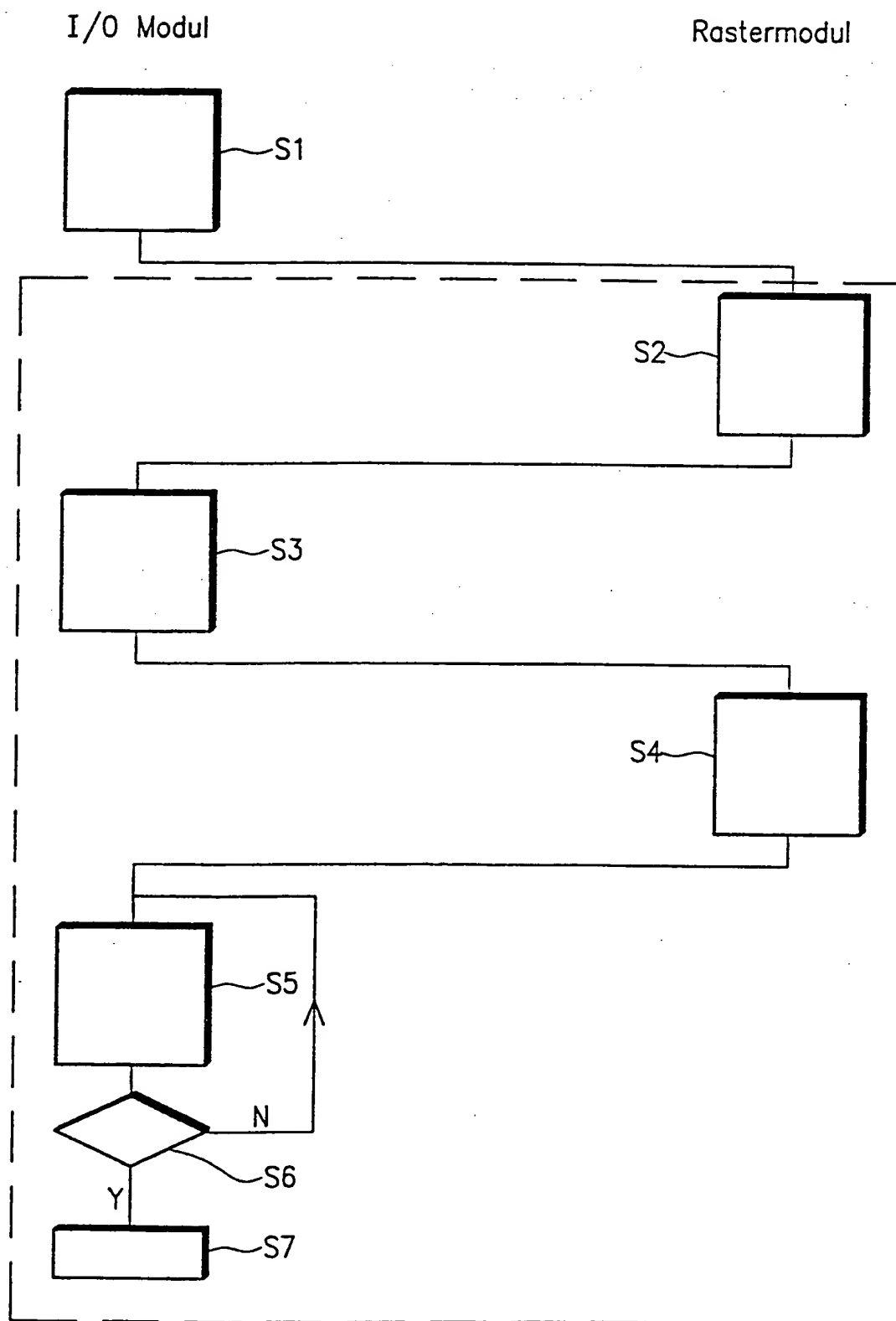


FIG.2

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102140" 225/0860

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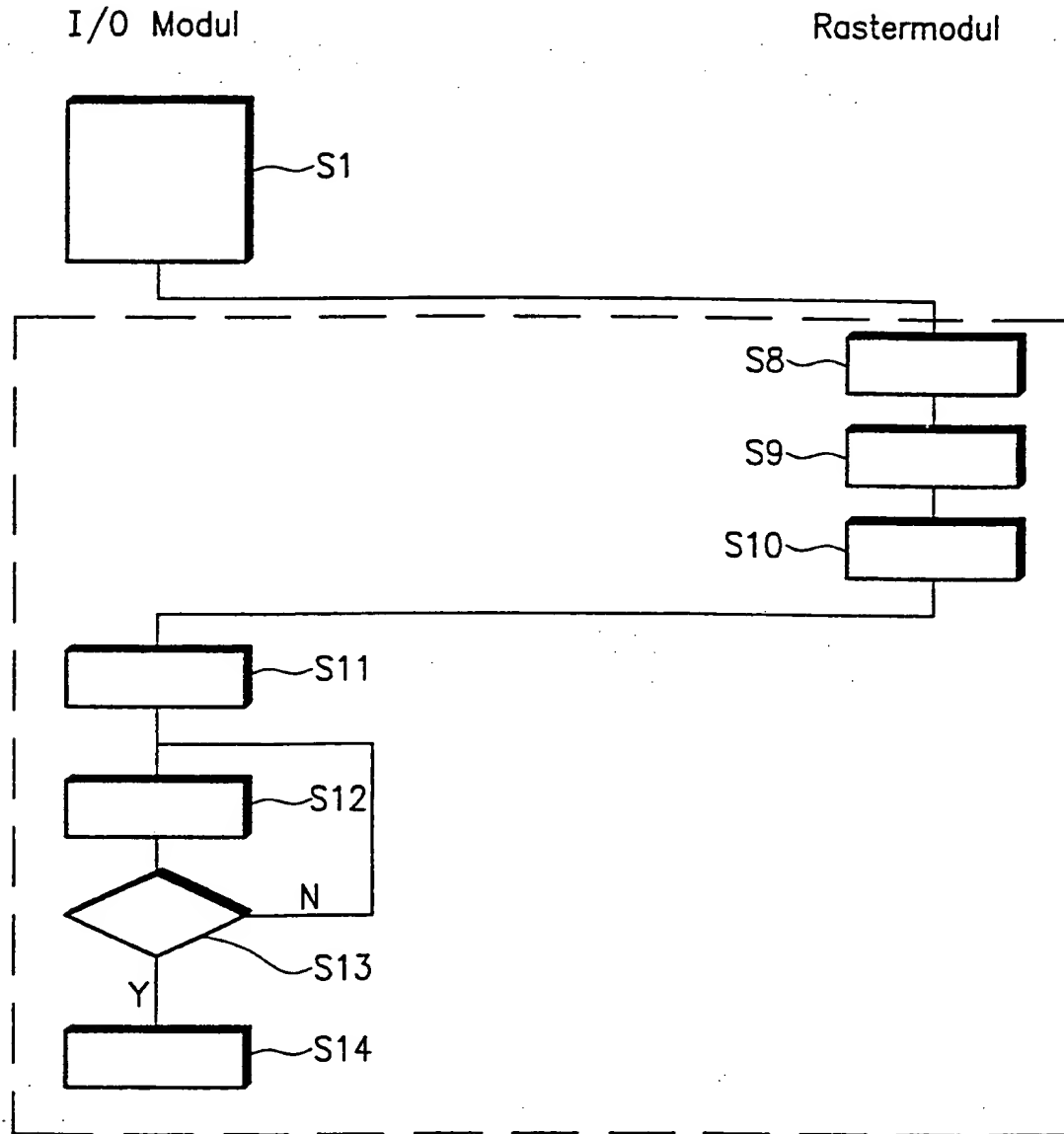


FIG. 4

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D<31..24>	D<23..16>	D<15..8>	D<7..0>
Reserved	Type=02	Source_addr.	Dest_addr.
data addr. in I/O-CPU			
Duty Cycle	No. data bytes		
Data byte 31	Data byte 30	Data byte 29	Data byte 28

FIG.5

D<31..24>	D<23..16>	D<15..8>	D<7..0>
		Source_addr.	Dest_addr.
			Type=3CH,3DH
Data byte 3	Data byte 2	Data byte 1	Data byte 0
Data byte 31	Data byte 30	Data byte 29	Data byte 28

FIG.6

	Byte 3	Byte 2	Byte 1	Byte 0
1		Duty Cycle	Source addr.	Destination addr.
2	DMA addr.			
3		Byte Count		

FIG.7

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